

Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet	1	of	3
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Application / Conf. No.	Unknown 101648,124
Filing Date	August 25, 2003
First Named Inventor	Aurelian Vasile Lazarut
Art Unit	Unknown 7826
Examiner Name	Unknown Len
Attorney Docket Number	X-1391 US

[illegible][illegible]

05/03/05

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application / Conf. No.	Unknown 101684,124
				Filing Date	August 25, 2003
				First Named Inventor	Aurelian Vasile Lazarut
				Art Unit	Unknown 2820
				Examiner Name	Unknown Levin
Sheet	2	of	3	Attorney Docket Number	X-1391 US

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
NL		Xilinx, Inc., "Speed Up Verification of Long Transaction Sequences with MicroBlaze Soft Processors"; Xcell Journal; Fall/Winter 2001; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124, pp. 34-35.	✓
		Xilinx, Inc.; "Bring on the Music - But Take out the Noise"; Xcell Journal; Spring 2002; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124, 2 pp.	✓
		HARDI Electronics AB; "HARDI ASIC Prototyping Board"; available at www.hardi.se; one page.	✓
		Mentor Graphics; "Co-Modeling Solutions"; Data Sheet available at www.mentor.com/emulation; two pages.	
		Aldec. "Desktop Prototyping Board - Testing the Possibilities of HDL Desktop"; available from www.aldec.com; two pages.	
		HARDI Electronics AB; "HARDI ASIC Prototyping System"; available at www.hardi.se; one page.	-
		Mentor Graphics; "iSOLVE In Circuit Verification Solutions"; Data Sheet; available at www.mentor.com; two pages.	
		Experience First, Inc.; "X1, Raptor 1.5 Virtex-E" Product Brief; available from Experience First, 3275 Stevens Creek Blvd., Suite 220, San Jose, CA 95117; one page.	
		EXP First, Inc.; "Raptor/ISD; August 8, 2003; four pages.	
		Experience First, Inc.; "Raptor/ISD 100"; Product Debut, Limited Release; May 2001; available at www.expfirst.com; one page.	
NL		Stohr, Bernd et al.; "A Complete SoC verification framework accelerating productivity through reuse and automation"; 9 pages.	✓

Examiner Signature	<i>[Signature]</i>	Date Considered	05/03/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application / Conf. No.	Unknown 101648,124
		Filing Date	August 25, 2003
		First Named Inventor	Aurelian Vasile Lazarut
		Art Unit	Unknown 2825
		Examiner Name	Unknown Levin
Sheet 3 of 3	Attorney Docket Number	X-1391 US	

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
NL		IKOS Systems, Inc.; Test Expert, "The Complete Verification Solution for Communication Designs"; Preliminary Data Sheet; available at www.ikos.com; two pages.	
		Mentor Graphics; "VStations-15M High-Performance System Integration Verification"; Data Sheet; available at www.mentor.com/emulation; two pages.	
		Mentor Graphics; "VStations-30M High-Performance System Integration Verification"; Data Sheet; available at www.mentor.com/emulation; four pages.	
		Xilinx, Inc.; "FlexBench Tool Suite Relies on Xilinx Silicon and Software"; Xcell Journal; Spring 2002; available from Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124; four pages.	
NL		EXP First, Inc.; Xcell Magazine Article Warren Miller, Rev 1.2; "A Hardware Reference Design for Xilinx Reed-Solomon LogiCore - Uses New IP Development Environment to Accelerate Simulation and Development"; August 8, 2003; three pages.	

Examiner Signature	<i>Mark Levin</i>	Date Considered	05/03/05
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